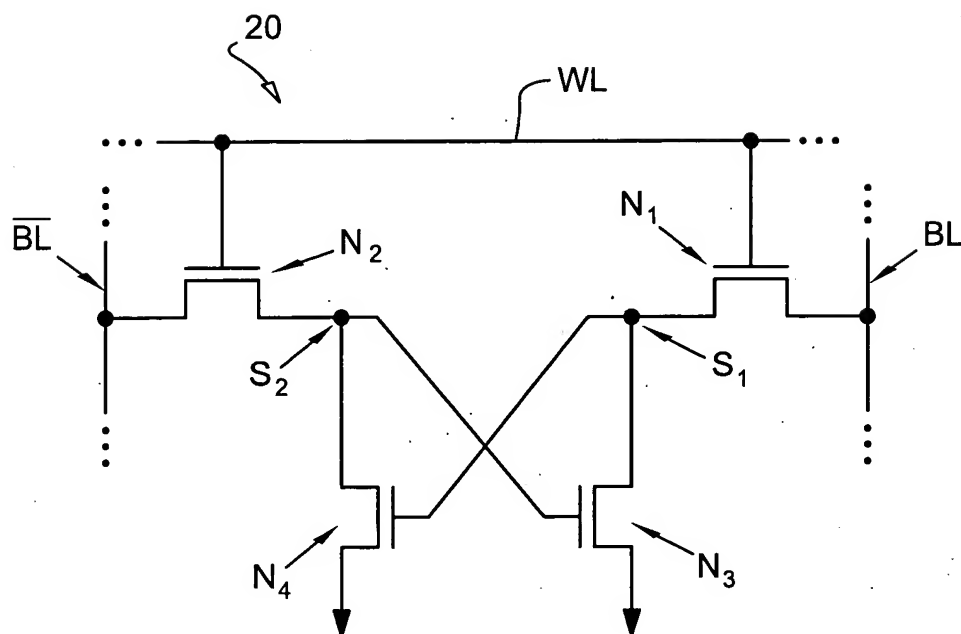
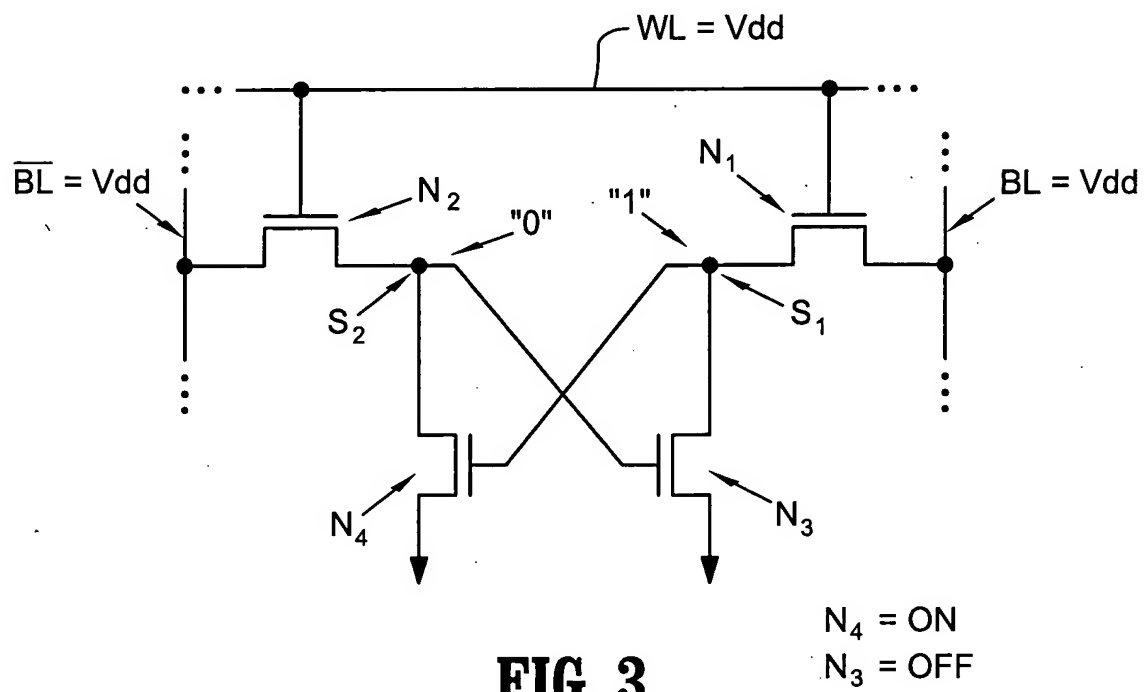


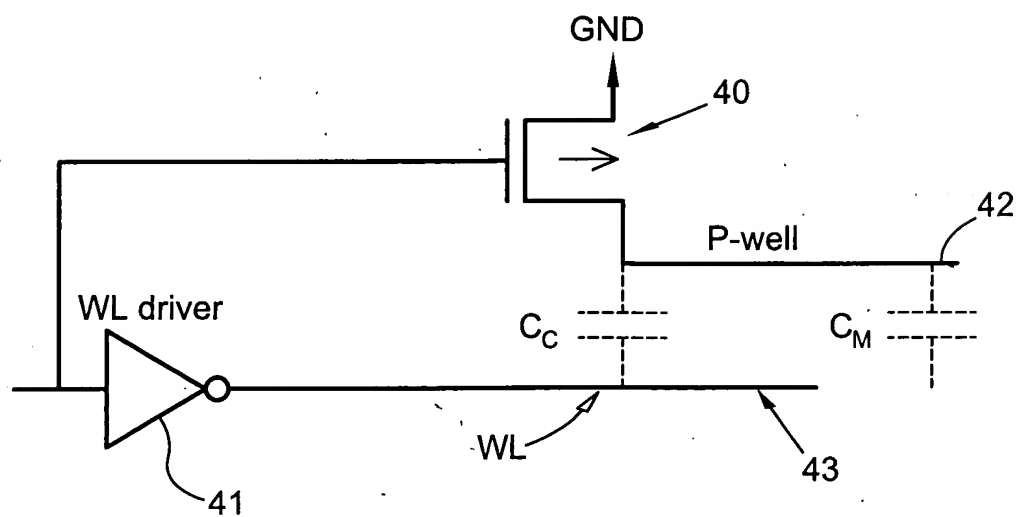
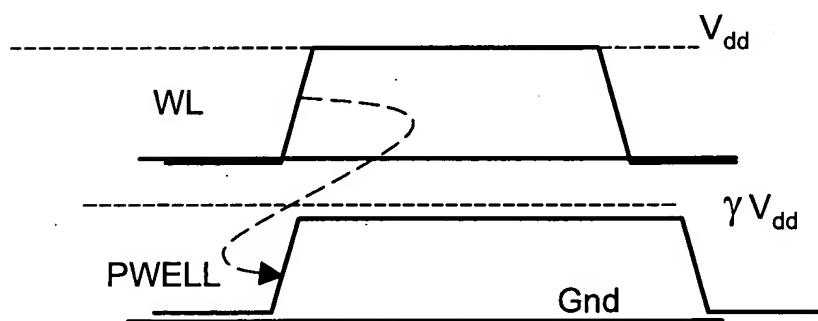
(Prior Art)  
**FIG. 1**



N<sub>1</sub> and N<sub>2</sub> - LVt NFETs  
N<sub>3</sub> and N<sub>4</sub> - HVt NFETs

**FIG. 2**



**FIG. 5****FIG. 6a**

$$V_{dd} \gamma = V_{dd} C_C / (C_C + C_M)$$

**FIG. 6b**